

## BACKGROUND OF INVENTION

Page 3, after line 21, please insert:

## SUMMARY OF INVENTION

Page 4, after line 10, please insert:

## BRIEF DESCRIPTION OF THE DRAWINGS

Page 4, after line 24, please insert:

## DETAILED DESCRIPTION

Replace the paragraph on page 7, beginning at line 20 with the following:

This cue at the output of the counter DW is dispatched to a level shifting circuit 17 and returned to the counter modulo  $N'$  15.

## IN THE CLAIMS

Please amend Claims 1-2 as follows. A marked-up copy of the amended claims is attached.

1. (Twice Amended) Process for displaying data on a matrix display having  $N$  data lines and  $P$  selection lines at the intersections of which are situated the image points or pixels, and in which the  $N$  data lines are grouped into  $P$  blocks of  $N'$  data lines each ( $N = P \times N'$ ), each block receiving in parallel one of the  $P$  data signals which is demultiplexed on the  $N'$  lines of said block, wherein, alternately, according to the selection lines, the scanning of the  $N'$  data lines of a block is carried out from 1 to  $N'$  and from  $N'$  to 1.

2. (Twice Amended) Process according to Claim 1, wherein the scan from 1 to  $N'$  then from  $N'$  to 1 is carried out every second selection line, the scan being carried out in a first direction for a first selection line and in a second direction for a succeeding selection line.